

Electronic Acknowledgement Receipt

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Application Number:	10829559
International Application Number:	
Confirmation Number:	6357
Title of Invention:	Method and apparatus for implementing a multiplier utilizing digital signal processor block memory extension
First Named Inventor/Applicant Name:	Asher Hazanchuk
Customer Number:	27296
Filer:	Lawrence M. Cho
Filer Authorized By:	
Attorney Docket Number:	ALT.P030 (A1252)
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	ALT_P030_A01252_Response_12_28_2010.pdf	56504 b93887fdb3405b662cbe12db52308d994e ec2c74	no	10

Warnings:

Information:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.